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Volnei A. Pedroni, from Caltech (USA) and UTFPr (Brazil).

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Prof. Volnei A. Pedroni. The author received his MSc and PhD degrees in Electrical Engineering from the California Institute of Technology (Caltech), with emphasis on VLSI design, analog and digital. He is a Professor of Electronics Engineering at Brazil's UTFPR and a regular Visiting Faculty of Electrical Engineering at Caltech. He has done

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collaboration also with other Institutions,
in USA and Italy (he is a Brazilian and
Italian citizen).

*About the Author | Circuit Design with VHDL
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The book is organized in a clear progression,
with the first part covering the circuit
level, treating foundations of VHDL and
fundamental coding, and the second part
covering the system level (units that might
be located in a library for code sharing,
reuse, and partitioning), expanding upon the
earlier chapters to discuss system

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